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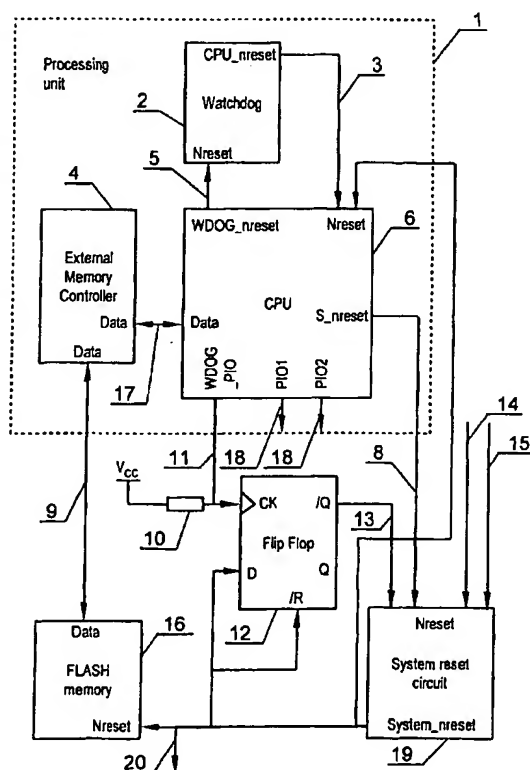
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(75) Inventor/Applicant (for US only): **STABROWSKI, Marcin** [PL/PL]; ul. Dabrowki 14 m. 3, PL-65-096 Zielona Góra (PL).
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- (71) Applicants (for all designated States except US): **ADVANCED DIGITAL BROADCAST POLSKA SP. ZO.O.** [PL/PL]; ul. Trasa Północna 16, PL-65-119 Zielona Góra (PL). **ADVANCED DIGITAL BROADCAST LTD.** [—/—]; 15F, 205, Section 3, Pei-Hsin Road, Hsin-Tien City, Taipei County, 231 Taiwan (TW).

[Continued on next page]

(54) Title: **CIRCUIT FOR DETECTION OF INTERNAL MICROPROCESSOR WATCHDOG DEVICE EXECUTION AND METHOD FOR RESETTING MICROPROCESSOR SYSTEM**



(57) Abstract: In a circuit for detection of internal microprocessor watchdog device execution comprising a microprocessor (6) with the internal watchdog device and with an input/output line (11) transmitting information about microprocessor reset, and a device for resetting the microprocessor system, to the input/output line (11) transmitting information about microprocessor (6) reset, a clock input CK is connected, which triggers the flip-flop (12), whose data input D and an inverted reset input /R are connected to the output of the device (19) for resetting the microprocessor system, and the inverted flip-flop (12) output /Q is connected to the input of the device (19) for resetting the microprocessor system.